### ABLNO

### **ESD** Sensitive

#### **FEATURES:**

- High "Q", 3rd Overtone Crystal Technology
- Ultra Low Phase Noise -162 dBc/Hz Typ. @ 10kHz offset, 100MHz carrier
- Standard LVCMOS RF Output
- Wide Operating Temperature (-40°C to +85°C) standard
- ±28 ppm Max. All inclusive Stability (including Aging) over 10-years
- Available Frequency range from 50MHz to 156.25MHz
- 9.2 x 14.8mm RoHS Compliant SMT package

#### **STANDARD SPECIFICATIONS**



#### **▷** APPLICATIONS:

- Satellite Modem Communication Systems
- COTS Military communications

(Pb) RoHS Compliant

- Avionics
- · Low Phase Noise Signal Sources
- High Definition TV
- Test & Measurement
- Ultra Low Jitter RF Communication Circuitry

| Parameters                     |                             |                   | Minimum  | Typical  | Maximum | Units            | Notes                                       |  |
|--------------------------------|-----------------------------|-------------------|--|--|---------|------------------|---|--|
| RF Output Frequency Range      |                             |                   | 50.00  |  | 156.250 | MHz              |   |  |
| Standard Available Frequencies |                             |                   | 50.00MHz<br>96.00MHz,<br>106.25MHz,<br>150.00M | Custom<br>frequencies<br>available upon<br>request |         |                  |   |  |
| S                              | Supply Voltage (Vdd)        |                   |  | 3.300  | 3.465   | Volts            |   |  |
| Current                        | 50MHz ~ 99.999MHz           |                   |  |  | 25.00   |                  |   |  |
| Drain                          | 100MHz ~ 149.999MHz         |                   |  |  | 35.00   | mA               |   |  |
|                                | $\geq$ 150.00MHz            |                   |  |  | 40.00   |                  |   |  |
|                                | Waveform                    |                   |  | LVCMOS   |         |                  |   |  |
|                                | Outpu                       | t Load            |  |  | 15      | pF               |   |  |
|                                | V <sub>OH</sub>             |                   |  |  |         | Volts            |   |  |
|                                | V                           | OL                |  |  | 0.1*Vdd | Ω                |   |  |
| Symmetry                       |                             |                   | 45   | 50   | 55      | %                |   |  |
|                                | Rise & Fall Times           |                   |  |  | 3.0     | ns               |   |  |
| Operating Temperature Range    |                             |                   | -40  |  | +85     | °C               |   |  |
|                                | Frequency Stability         |                   |  |  |         |                  |   |  |
|                                | Over (-40° C to +85°C)      |                   |  | ±12.00   | ±18.00  | ppm              | Relative to<br>measured<br>frequency @ 25°C |  |
| AL                             | L effects                   | , including Aging |  |  | ±28.00  | ppm              |   |  |
| St                             | Storage Temperature Range   |                   |  |  | +90     | °C               |   |  |
|                                |                             | First Year        |  |  | ±2.00   | ppm              |   |  |
| Aging                          |                             | 5-Years           |  |  | ±5.00   | ppm              |   |  |
|                                |                             | 10-Years          |  |  | ±7.00   | ppm              |   |  |
| Phase                          | Phase Noise (50MHz Carrier) |                   |  |  |         |                  | Vdd=3.3V                                    |  |
| (                              | (a) 10 Hz offset            |                   |  | -90  | -82     | dBc/Hz           | Note #1 & #2                                |  |
| (a) 100 Hz offset              |                             |                   |  | -120   | -115    | dBc/Hz           |   |  |
| @ 1,000 Hz offset              |                             |                   |  | -145   | -140    | dBc/Hz           |   |  |
| @ 10,000 Hz offset             |                             |                   |  | -165   | -160    | dBc/Hz           |   |  |
| @ 100,000 Hz offset            |                             |                   |  | -166   | -165    | dBc/Hz           |   |  |
| @ 1,000,000 Hz offset          |                             |                   |  | -166   | -165    | dBc/Hz           |   |  |
| rms Jitter (12kHz ~ 20MHz BW)  |                             |                   |  | < 100  | 125     | Femto<br>Seconds | 0.125 ps Max.                               |  |



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(Pb) RoHS Compliant



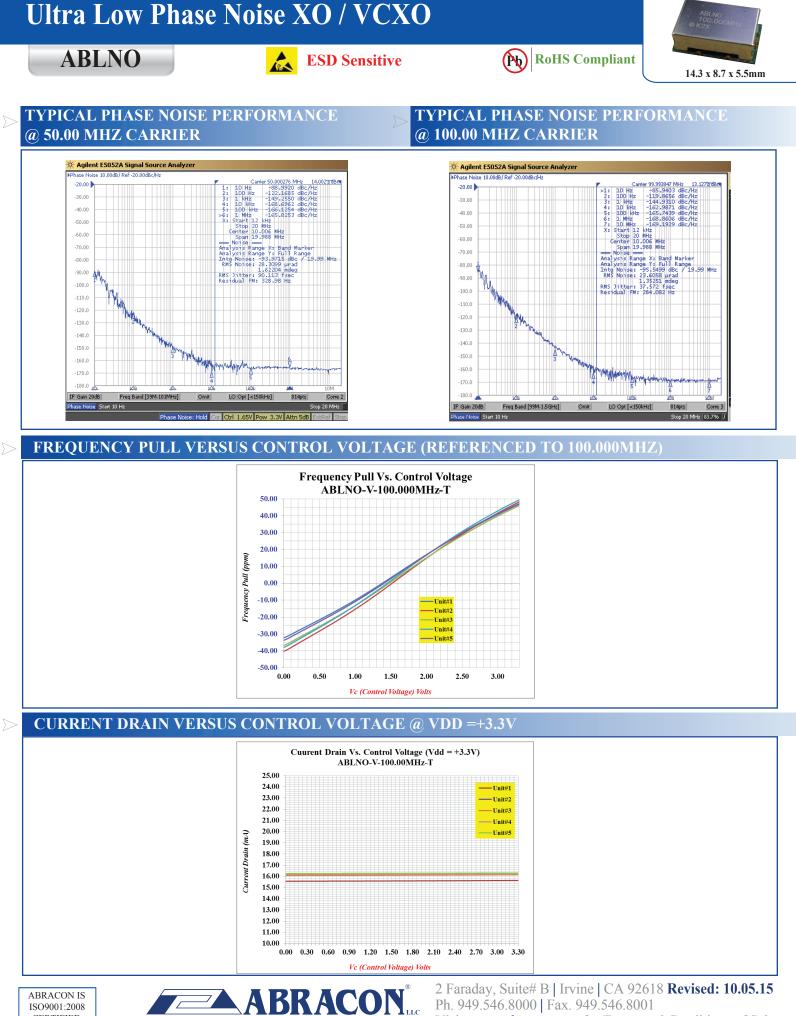
| Parameters                         | Minimum | Typical  | Maximum | Units            | Notes                     |
|------------------------------------|---------|----------|---------|------------------|---------------------------|
| Phase Noise (100MHz Carrier)       |         |          |         |                  | Vdd=3.3V                  |
| @ 10 Hz offset                     |         | -88      | -82     | dBc/Hz           | Note #1 & #2              |
| @ 100 Hz offset                    |         | -118     | -115    | dBc/Hz           |                           |
| @ 1,000 Hz offset                  |         | -141     | -138    | dBc/Hz           |                           |
| @ 10,000 Hz offset                 |         | -160     | -155    | dBc/Hz           |                           |
| @ 100,000 Hz offset                |         | -161     | -160    | dBc/Hz           |                           |
| @ 1,000,000 Hz offset              |         | -165     | -160    | dBc/Hz           |                           |
| rms Jitter (12kHz ~ 20MHz BW)      |         | < 50     | 100     | Femto<br>Seconds | 0.10 ps Max.              |
|                                    |         |          |         |                  |                           |
| Phase Noise (156.25MHz<br>Carrier) |         |          |         |                  | Vdd=3.3V                  |
| @ 10 Hz offset                     |         | -75      | -70     | dBc/Hz           | Note #1 & #2              |
| @ 100 Hz offset                    |         | -110     | -105    | dBc/Hz           |                           |
| @ 1,000 Hz offset                  |         | -140     | -135    | dBc/Hz           |                           |
| @ 10,000 Hz offset                 |         | -155     | -150    | dBc/Hz           |                           |
| @ 100,000 Hz offset                |         | -161     | -160    | dBc/Hz           |                           |
| @ 1,000,000 Hz offset              |         | -165     | -160    | dBc/Hz           |                           |
| rms Jitter (12kHz ~ 20MHz BW)      |         | < 50     | 100     | Femto<br>Seconds | 0.10 ps Max.              |
|                                    |         |          |         |                  |                           |
| Electrical Frequency Adjustment    |         |          |         |                  |                           |
| Control Voltage Range (Vc)         | 0.0     | 1.65     | 3.30    | Volts            |                           |
| Frequency Pull Range               | ±28.00  |          | ±55.00  | ppm              | Referenced to the carrier |
| Frequency Pull Slope               |         | Positive |         |                  |                           |
| Control Voltage Port Impedance     | 10      |          |         | kΩ               |                           |
| Control Port Linearity             |         |          | ±10     | %                |                           |

*Note #1:* Maximum Phase Noise is verified on 100% of the parts at  $25^{\circ}C \pm 3^{\circ}C$ .





*Note #2:* The above specified Phase Noise & Jitter is with the oscillator device configured as a VCXO. In XO configuration, the Phase Noise will be slightly better at each offset between 10Hz and 10 kHz, by approximately -3dB to -5dB.



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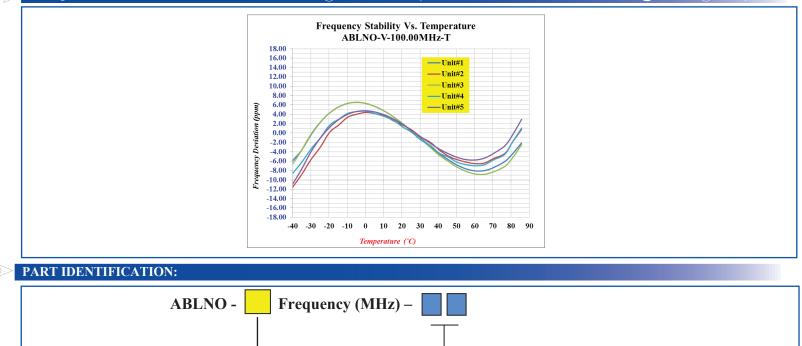


<u>≳</u> ESD Sensitive



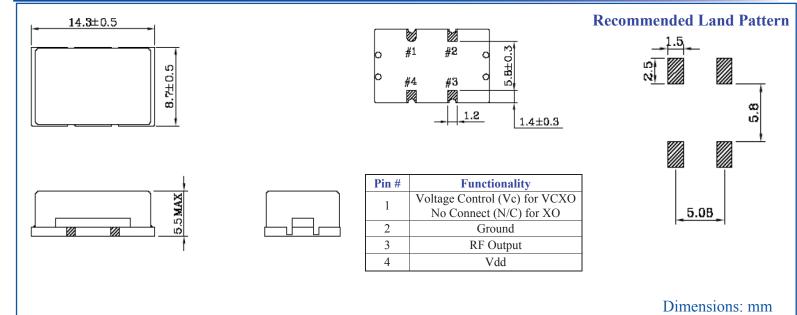


FREQUENCY STABILITY VS. TEMPERATURE @ VDD = +3.3V (REFERENCED TO MEASURED FREQUENCY @ 25°C)



| Fixed | Fixed Clock Vs. VCXO Option |  |       | Tape & Reel Options     |  |  |
|-------|-----------------------------|--|-------|-------------------------|--|--|
| Blank | Fixed Clock Oscillator      |  | Blank | < 250 units on cut tape |  |  |
| V     | VCXO (±28 ppm min. Pull)    |  | T2    | 250 units per reel      |  |  |
|       |                             |  | Т     | 1,000 units per reel    |  |  |

**OUTLINE DIMENSIONS:** 



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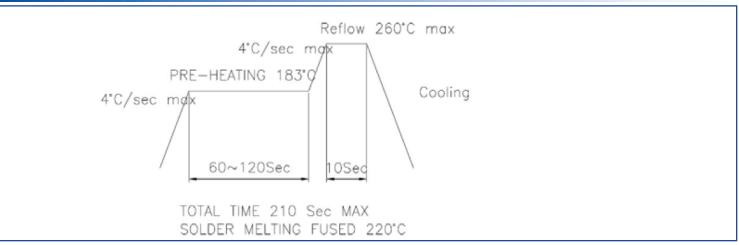
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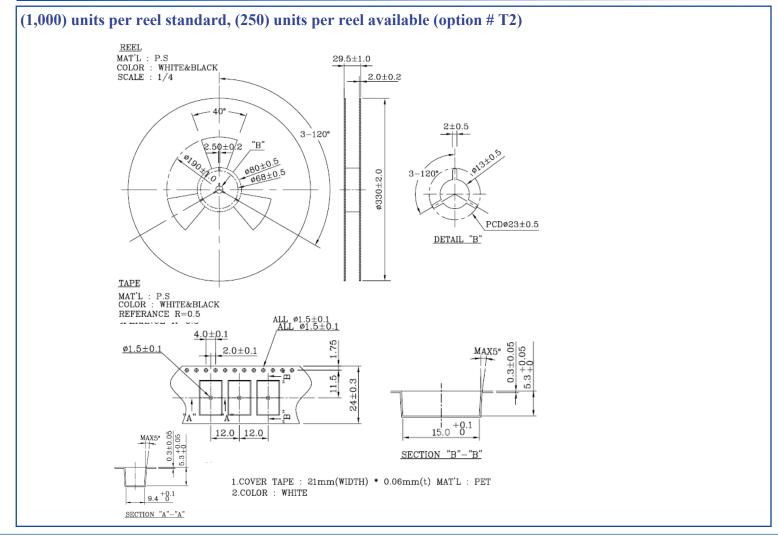




#### **REFLOW PROFILE:**



#### **TAPE & REEL:**



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